

# INTERNATIONAL STANDARD

# NORME INTERNATIONALE

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**Function blocks (FB) for process control and electronic device description  
language (EDDL) –  
Part 5: EDDL Built-in library**

**Blocs fonctionnels (FB) pour les procédés industriels et le langage de  
description électronique de produit (EDDL) –  
Partie 5: Bibliothèque de Built-in EDDL**



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## INTERNATIONAL ELECTROTECHNICAL COMMISSION

**FUNCTION BLOCKS (FB) FOR PROCESS CONTROL AND  
ELECTRONIC DEVICE DESCRIPTION LANGUAGE (EDDL) –****Part 5: EDDL Builtin library**

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International Standard IEC 61804-5 has been prepared by subcommittee 65E: Devices and integration in enterprise systems, of IEC technical committee 65: Industrial-process measurement, control and automation.

The text of this standard is based on the following documents:

FDIS	Report on voting
65E/450/FDIS	65E/461/RVD

Full information on the voting for the approval of this standard can be found in the report on voting indicated in the above table.

This publication has been drafted in accordance with the ISO/IEC Directives, Part 2.

A list of all parts in the IEC 61804 series, published under the general title *Function blocks (FB) for process control and Electronic Device Description Language (EDDL)*, can be found on the IEC website.

Future standards in this series will carry the new general title as cited above. Titles of existing standards in this series will be updated at the time of the next edition.

The committee has decided that the contents of this publication will remain unchanged until the stability date indicated on the IEC web site under "<http://webstore.iec.ch>" in the data related to the specific publication. At this date, the publication will be

- reconfirmed,
- withdrawn,
- replaced by a revised edition, or
- amended.

## INTRODUCTION

This part of IEC 61804 is an extension to IEC 61804-3. In earlier editions of IEC 61804-3 the EDDL Built-in library was part of the Electronic Device Description Language (EDDL) document IEC 61804-3. Because of editorial problems, the EDDL Built-in library is now moved to this separate part of IEC 61804.

# FUNCTION BLOCKS (FB) FOR PROCESS CONTROL AND ELECTRONIC DEVICE DESCRIPTION LANGUAGE (EDDL) –

## Part 5: EDDL Builtin library

### 1 Scope

This part of IEC 61804 specifies the EDDL Builtin library and provides the profiles of the various fieldbuses.

### 2 Normative references

The following documents, in whole or in part, are normatively referenced in this document and are indispensable for its application. For dated references, only the edition cited applies. For undated references, the latest edition of the referenced document (including any amendments) applies.

IEC 61804-3:2015, *Function blocks (FB) for process control and Electronic Device Description Language (EDDL) – Part 3: EDDL syntax and semantics*

IEEE 754, *Standard for Binary Floating-Point Arithmetic for microprocessor systems*

### 3 Terms, definitions, acronyms and abbreviated terms

#### 3.1 Terms and definitions

For the purposes of this document, the terms and definitions given in IEC 61804-3 apply.

#### 3.2 Acronyms and abbreviated terms

CP	Communication profile
CPF	Communication profile family
DD	Device Description
EDD	Electronic Device Description
EDDL	Electronic Device Description Language
HCF	HART <sup>®</sup> 1 Communication Foundation
I/O	Input/output
ID	Identifier

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<sup>1</sup> HART<sup>®</sup> is a registered trademark of the consortium HART Communication Foundation (HCF) (non-profit organization). This information is given for the convenience of users of this standard and does not constitute an endorsement by IEC of the trade name holder or any of its products. Compliance to this profile does not require use of the trade name. Use of the trade names requires permission from the trade name holder.